

2863

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TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>	Application / Conf. No.	10/021,447 / 6106
	Filing Date	October 30, 2001
	First Named Inventor	Siuki Chan
	Examiner Name	Aditya Bhat
	Art Unit	2863
	Patent No.	
Mail Stop: NON-FEE AMENDMENT	Attorney Docket Number	X-885 US
Express Mail Receipt No.		
Total Number of Pages in This Submission	13	

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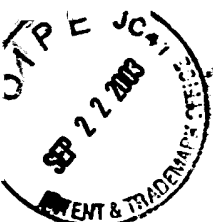
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X-885 US
10/021,447

PATENT
Conf. No.: 6106

4/A
P. Ullrich
9-29-03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Siuki Chan
Assignee: Xilinx, Inc.
Title: "METHODS AND CIRCUITS FOR MEASURING CLOCK SKEW
ON PROGRAMMABLE LOGIC DEVICES"
Serial No.: 10/021,447 Filing Date: 10/30/2001
Examiner: Bhat, Aditya Art Unit: 2863
Docket No.: X-885 Conf. No.: 6106

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AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION

Sir:

In response to the First Office Action mailed from the Patent Office on July 10, 2003 (hereafter "OA"), please amend the above-identified application as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks/Arguments begin on page 8 of this paper.